

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization
International Bureau



(43) International Publication Date
20 March 2003 (20.03.2003)

PCT

(10) International Publication Number
WO 03/023705 A1

(51) International Patent Classification⁷: G06K 19/077, 7/00, 19/04, G01S 13/00

(21) International Application Number: PCT/US02/28921

(22) International Filing Date: 11 September 2002 (11.09.2002)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
60/318,731 13 September 2001 (13.09.2001) US
10/224,177 20 August 2002 (20.08.2002) US

(63) Related by continuation (CON) or continuation-in-part (CIP) to earlier application:
US 10/224,177 (CON)
Filed on 20 August 2002 (20.08.2002)

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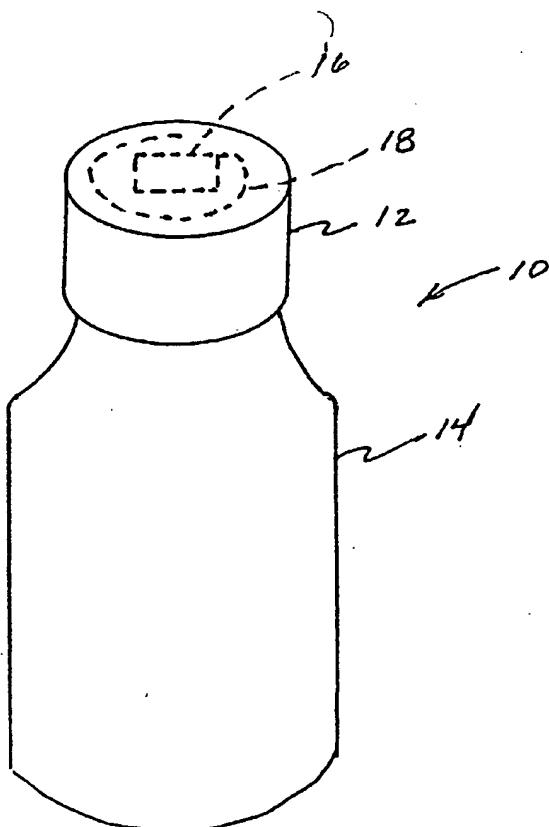
(81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU,

[Continued on next page]

(54) Title: METHOD OF MAKING INTERACTIVE INFORMATION CLOSURE



WO 03/023705 A1



(57) Abstract: A method of making an interactive information package (10), including an interactive information closure (12) including a radio frequency identification device (16, 18), contemplates that a microelectronics assembly be provided, and positioned on an associated substrate for positioning adjacent an inside surface of the top wall portion of the closure of the package. In one embodiment, the mounting substrate is provided in the form of a disc-shaped sealing liner for the closure (12). In an alternate embodiment, the mounting substrate is laminated to an associated sealing liner, with the substrate, and microelectronics assembly positioned thereon, inserted together with the sealing liner into the associated molded closure (12).



CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, OM, PH, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

— *with international search report*

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(84) **Designated States (regional):** ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE,

METHOD OF MAKING INTERACTIVE INFORMATION CLOSURE

The present invention relates generally to a method of making an interactive information package comprising a closure and container, with the package including a microelectronics assembly, and more particularly to a method of making 5 such a package by employing a mounting substrate for disposition of the microelectronics assembly adjacent to the inside surface of a top wall portion of the closure of the package.

The development of radio frequency identification, often called RDID, integrated circuitry has permitted use of such devices in a wide range of applications. 10 Use of such arrangements in a product package comprising a closure and container has a wide variety of applications, including product promotions, storage and dissemination product information including product processing, and quality assurance, including tamper-indication, by monitoring the conditions within the product package. U.S. Patent Application Serial No. 60/291,916, filed May 18, 2001, 15 hereby incorporated by reference, discloses a product package including a closure and container, wherein the closure includes a microelectronics assembly configured for interaction with associated radio frequency "writers" and scanners.

A microelectronics assembly such as disclosed in the above-referenced patent application is configured for radio frequency interaction by the provision of a 20 suitable radio frequency identification (RFID) integrated circuit, an antenna, and one or more interconnections operatively connecting the circuit and the antenna. The microelectronics assembly may include one or more microsensors, as well as a self-contained power source.

Cost-effective use of such interactive devices in packages including 25 closures and containers requires that the microelectronics assembly be efficiently and economically positioned in the package, preferably adjacent the inside surface of a top wall portion of the package's closure. In the past, it has been common practice to supply microelectronics, and in particular RFID tags, embedded in or attached to a plastic or paper substrate. Such a plastic or paper substrate is typically supplied in 30 large rolls to manufacturers of so-called "smart" products, such as smart credit cards and the like. Suitable machinery has been developed whereby the microelectronics assemblies can be efficiently inserted into products, such as by detachment from the substrate, or alternatively, by insertion of a portion of the substrate along with the microelectronics assembly into the product being manufactured.

35 The present invention is directed to a method of making an interactive information package, including a closure and container, wherein a microelectronics assembly is mounted on a mounting substrate which is subsequently inserted into the

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package's closure for use in association with the contents of the package.

A method of making an interactive information closure for a package contemplates the provision of a microelectronics assembly and a mounting substrate upon which the microelectronics assembly is positioned. The substrate is then inserted
5 into a plastic closure of the package for disposition adjacent to the inside surface of the top wall portion thereof. As disclosed herein, the mounting substrate may comprise a sealing liner for the plastic closure, which engages the associated package container to form a seal. Alternatively, the mounting substrate, with the microelectronics package positioned thereon, can be laminated to a disc-shaped sealing liner for the closure, with
10 the laminated assembly thereafter inserted into the closure.

A method of making an interactive information closure embodying the principles of the present invention comprises the steps of molding a plastic closure having a top wall portion, and an annular depending skirt portion. The method further includes providing a microelectronics assembly which includes a radio frequency
15 identification (RFID) integrated circuit and antenna, and one or more interconnections operatively connecting the circuit and the antenna.

The present method further includes providing a mounting substrate, and positioning the microelectronics assembly on the substrate. The mounting substrate, with the microelectronics assembly thereon, is thereafter inserted into the
20 plastic closure for disposition adjacent to the inside surface of the top wall portion. As noted, the mounting substrate may be configured to provide the sealing liner for the plastic closure, and is thus configured for sealing cooperation with an associated container of the package after the closure is applied thereto after the container is filled with its contents. Alternatively, the present invention contemplates providing a
25 disc-shaped sealing liner for the closure, and laminating the mounting substrate, with the microelectronics assembly thereon, to the sealing liner. The inserting step of the present method thus includes inserting the laminated sealing liner and mounting substrate into the plastic closure together.

The present invention contemplates various techniques for effecting the
30 desired mounting of the microelectronics assembly on the substrate. It is contemplated that the antenna and interconnections of the assembly can be printed on the substrate with electrically conductive inks, with the printing steps selected from the group consisting of ink jet printing, silk screen printing, and offset printing. Alternatively, the antenna and interconnections can be formed on the substrate by thin film
35 deposition utilizing evaporation or sputtering. Etching and/or laser machining of the thin film is performed to form the antenna and interconnections.

Alternatively, the antenna and interconnections of the microelectronics

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assembly can be formed on the mounting substrate by lamination, followed by etching and/or laser machining of the lamination to form the antenna and interconnections.

The RFID integrated circuit of the microelectronics assembly can be positioned on the substrate by various techniques. Such techniques include positioning 5 the integrated circuit active-side-down on the substrate with the pads of said integrated circuit directly connected to the antenna or interconnections by soldering, stud-bump bonding or with conductive adhesives, or alternatively, active-side-up on a substrate with connection from the pads of the integrated circuit to the antenna or interconnections made by wire bonding. By an alternative technique, the integrated 10 circuit can be positioned on the substrate, with a planarization layer formed over the integrated circuit. Formation of one or more openings in the planarization layer is effected, with formation of the antenna on the planarization layer, and formation of the interconnections through the openings in the planarization layer. The antenna and interconnections can be formed by metal deposition followed by photolithography, 15 with the formation of openings in the planarization layer effected by either photolithography or laser machining.

For some applications, it can be desirable to form the electronics package on the mounting substrate, by printing the integrated circuit with semi-conductor inks and the antenna, and the interconnections with 20 electrically-conductive inks on the substrate.

An apparatus is disclosed herein for effecting insertion of a substratemounted microelectronics assembly into an associated closure cap. It is contemplated that such an apparatus not only effects the insertion of the microelectronics assembly and substrate into the closure, but that the apparatus be 25 configured to effect reading of information from, and writing of information onto, the assembly, with the apparatus most importantly testing the installed microelectronics assembly prior to shipment.

Other features and advantages of the present invention will become readily apparent from the following detailed description, the accompanying drawings, 30 and the appended claims.

FIGURE 1 is a diagrammatic view of an interactive information package of the type that can be formed in accordance with the present invention, including a plastic closure and associated container;

FIGURE 2 is a diagrammatic view of a radio frequency integrated 35 circuit system for providing a wide variety of applications, including product promotions, storage and dissemination product information including product processing, and quality assurance, including tamper-indication, by monitoring the

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conditions within the product package; and

FIGURE 3 is a diagrammatic view of an apparatus for practicing the method of the present invention for insertion of a microelectronics assembly and mounting substrate into an associated closure.

5 While the present invention is susceptible of embodiment in various forms, there is shown in the drawings, and will hereinafter be described, a presently preferred embodiment, with the understanding that the present disclosure is to be considered as an exemplification of the invention, and is not intended to limit the invention to the specific embodiments disclosed herein.

10 The present invention is directed to a method of making an interactive information package including a plastic closure and container, wherein a radio frequency identification integrated circuit and associated antenna are disposed within the package, preferably by disposition on or at the interior surface of the package closure. The arrangement of the present package facilitates its use for a variety of 15 applications, including product promotion, storage and dissemination of product information including product processing information, and product quality assurance, including tamper-evidence. Use of packages formed in accordance with the present invention permits efficient inventory control, by permitting product purchases to be efficiently tracked without resorting to optical scanning of bar codes and the like.

20 As illustrated in FIGURE 1, the package 10 which can be formed in accordance with the present invention comprises plastic closure 12 and an associated container 14 to which the closure can be secured, such as by cooperating, inter-engaged thread formations. Plastic closure 12 can be injection molded or compression molded, with U.S. Patent No. 4,497,765, hereby incorporated by 25 reference, disclosing a method and apparatus for efficiently compression-molding closures of this type.

The package 10 includes a radio frequency identification integrated circuit 16 (sometimes referred to as a radio frequency identification (RFID) device or tag) disposed within the package 10, preferably by disposition on or at the interior 30 surface of the closure 12, that is, adjacent the inside surface of a top wall portion of the closure. The arrangement includes an antenna 18 operatively connected to the integrated circuit 16, with the antenna cooperating with the integrated circuit to permit the integrated circuit to be externally powered without physical connection of a power supply thereto. The antenna 18 provides the desired radio frequency interface with an 35 associated radio frequency input/output device 20 (FIGURE 2) which can be configured to provide remote input to and/or reading and retrieval of electronic information carried by the integrated circuit 16.

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For purposes of the present disclosure, the term microelectronics, and microelectronics assembly, includes, but is not limited to RFID supporting electronics, antenna to support radio frequency communication, micro-sensors, and micro-power sources.

5 In accordance with the present disclosure, it is contemplated that the integrated circuit 16 and antenna 18, and any associated components, including interconnections with the integrated circuit, be positioned within the closure 12 by disposition of the electronic components (referred to herein as a microelectronics assembly) by the provision of a substrate which is inserted into the closure 12. As will
10 be further described, the mounting substrate for the microelectronics assembly can be inserted into the closure 12 for disposition adjacent the inside surface of the top wall portion thereof. It is contemplated that the mounting substrate can be provided in the form of a sealing liner for the closure, or alternatively, that the mounting substrate be laminated to an associated sealing liner, with the substrate and liner (with the
15 microelectronics assembly secured thereto) inserted as a unit into the associated closure 12.

In the embodiment illustrated in FIGURE 1, external powering of the integrated circuit precludes the need for an internal power supply operatively connected to the integrated circuit for providing electrical power thereto. However, for
20 some configurations of the present package (such as providing tamper-evidence indication or capturing continuous historical data on package parameters such as pressure and/or temperature), it can be desirable to provide a compact power supply 22, such as diagrammatically illustrated in FIGURE 2, operatively connected to the integrated circuit 16.

25 The microelectronics assembly of the package can be configured to include one or more different types of compact-size (i.e., micro) sensing devices. Such sensing devices may include, by way of example, a pressure sensor 24, a temperature sensor 26, a chemical sensor 28 for sensing the presence of chemicals such as oxygen, and/or a biological sensor 30 for sensing the presence of microorganisms within the
30 package 10. The configuration of the present package with one or more of the internal sensing devices greatly enhances versatile use of the present package for quality assurance functions, including tamper-evidence, brand protection, and anti-counterfeiting. An array of sensors can be provided for certain applications, with the array preferably integrated with radio frequency integrated circuit 16. The
35 provision of one or more sensors permits direct assessment of the quality state of the packaged goods.

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reference, further describes use of the package 10 for specific applications.

It is contemplated that the microelectronics assembly used in practicing the present invention can be positioned on a mounting substrate inserted into closure 12, apart from any sealing liner or like component provided in the closure. The 5 present invention particularly contemplates that the mounting substrate for the microelectronics be provided in the form of, or be secured to, a sealing liner for the closure 12. For plastic closures lined with loose disc liners inserted into pre-molded closures (as opposed to *in situ* molding of a liner), closure manufacturers typically use thin sheet lining material ranging from 0.015 to 0.030 inches thick, depending upon the 10 particular closure design. This lining material is supplied in large rolls, and is typically fed into punching machines that punch circular discs from the lining material, and substantially simultaneously insert the punched discs into closure shells. The remaining "skeleton" is typically re-ground and returned to the material supplier for recycling and inclusion in future rolls of lining material.

15 As noted, the present invention is directed to a method of making an interactive information closure, which method contemplates the molding of a plastic closure having a top wall portion and an annular depending skirt portion, such as illustrated closure 12. The present invention further contemplates that a microelectronics assembly, including an RFID integrated circuit, an antenna, and one 20 or more interconnections operatively connecting the circuit and the antenna, be provided, with a mounting substrate provided for effecting insertion of the microelectronics package into the associated closure.

With reference to FIGURE 3, an apparatus 50 is illustrated whereby 25 microelectronics packages 52 are positioned on a sheet of mounting substrate material 54 with the apparatus 50 including a punch 56 for cutting the mounting substrate material, and inserting each individual mounting substrate, with microelectronics positioned thereon, into an associated closure 12. In this illustrated embodiment, it is contemplated that the mounting substrate material 54 be provided in the form of lining material for the formation of a mounting substrate in the form of a sealing liner for the 30 closure 12. For those applications in which the closure 12 does not require a sealing liner, the mounting substrate, and associated microelectronics, can be similarly inserted into the associated closure 12, and suitably adhered to the inside surface of the top wall portion of the closure, such as by a suitable adhesive or the like. For these 35 applications, the microelectronics are positioned between the top wall of the closure and the substrate, and are thus isolated from the contents of the package, in accordance with FDA requirements.

As noted, for some applications it can be desirable to provide the

mounting substrate for the microelectronics separately from the disc-shaped sealing liner for the closure 12. For such applications, the present invention contemplates that the mounting substrate be laminated to the disc-shaped sealing liner for the closure, with the laminated assembly thus formed being inserted into the associated closure

5 such that the mounting substrate (and microelectronics thereon) are positioned in the closure between the sealing liner and the inside surface of the top wall portion of the closure. In such an arrangement, the sealing liner thus acts to provide the desired sealing engagement between the closure assembly and the associated container, with the microelectronics assembly thus securely positioned within the package, yet isolated

10 from the contents therein, in accordance with FDA requirements.

The present invention contemplates that various techniques can be employed for providing the antenna 18, and associated interconnections, on the mounting substrate for the electronics assembly. In one form, the antenna and interconnections are printed on the substrate with electrically conductive inks, with the

15 printing steps selected from the group consisting of ink jet printing, silk screen printing, and offset printing. Alternatively, the antenna and interconnections can be formed by thin film deposition utilizing evaporation or sputtering on the mounting substrate, with etching or laser machining of the thin film effected to form the antenna and interconnections.

20 Other techniques can be employed in accordance with the present invention for formation of the antenna and interconnections of the microelectronics assembly. The antenna and interconnections can be formed by lamination on the mounting substrate, with the lamination etched or laser machined for formation of the antenna and interconnections. Laser "writing" can be employed through the use of

25 organo-metallic gas which forms metal deposits when subjected to laser light.

Mounting of the integrated circuit 16 on the mounting substrate can also be effected in various ways. As is known by those skilled in the art, the integrated circuit can be positioned active-side-down on the mounting substrate with connection from the pads on the integrated circuit made directly to the antenna or interconnection

30 by soldering, stud-bump bonding or with a conductive adhesive, or active-side-up on the substrate with connection from the pads on the integrated circuit made directly to the antenna or interconnection with wire bonds. Formation of the microelectronics assembly can include the steps of first positioning the integrated circuit on the mounting substrate, and forming a planarization layer over the integrated circuit. One

35 or more openings are then formed in the planarization layer, such as by photolithography or laser machining. The antenna is then formed on the planarization layer, and interconnections formed through the openings in the layer. The antenna and

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interconnections can be formed by metal deposition followed by photolithography.

It is within the purview of the present invention that the microelectronics assembly can be positioned on the mounting substrate by printing the integrated circuit with semi-conductor inks as well as the associated antenna and

5 interconnections with electrically-conductive inks directly on the substrate.

To facilitate efficient use of the present interactive information package, it is contemplated that the apparatus 50 employed for insertion of the microelectronics and mounting substrate into the associated closure be a so-called "smart machine", that is, capable of reading information from, and writing information onto, the

10 microelectronics assembly. It is particularly contemplated that this apparatus be configured for testing the installed microelectronics prior to shipment of the interactive information package.

CLAIMS

1. A method of making an interactive information closure, comprising the steps of:
 - 5 molding a plastic closure having a top wall portion, and an annular depending skirt portion;
 - 10 providing a microelectronics assembly, including an RFID integrated circuit, an antenna, and one or more interconnections operatively connecting said circuit and said antenna;
 - 15 providing a mounting substrate, and positioning said microelectronics assembly on said substrate; and inserting said mounting substrate with said microelectronics package thereon into said plastic closure for disposition adjacent to the inside surface of said top wall portion.
2. A method of making an interactive information closure in accordance with claim 1, wherein:
 - 15 said mounting substrate provides a sealing liner for said plastic closure.
3. A method of making an interactive information closure in accordance with claim 1, including:
 - 20 providing a disc-shaped sealing liner for said plastic closure, and laminating said mounting substrate with said microelectronics assembly thereon to said sealing liner,
 - 25 said inserting step including inserting said laminated sealing liner and mounting substrate into said plastic closure.
4. A method of making an interactive information closure in accordance with claim 1, including:
 - 30 printing said antenna and said interconnections on said substrate with electrically-conductive inks.
5. A method of making an interactive information closure in accordance with claim 4, wherein:
 - 35 said printing step is selected from the group consisting of ink jet printing, silk screen printing, and offset printing.
6. A method of making an interactive information closure in accordance with claim 1, including:
 - 40 forming said antenna and said interconnections by thin film deposition on said mounting substrate.
7. A method of making an interactive information closure in accordance with claim 6, including:

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etching said thin film to form said antenna and interconnections.

8. A method of making an interactive information closure in accordance with claim 6, including:

laser machining said thin film to form said antenna and

5 interconnections.

9. A method of making an interactive information closure in accordance with claim 1, including:

forming said antenna and said interconnections by lamination on said mounting substrate.

10 10. A method of making an interactive information closure in accordance with claim 9, including:

etching said lamination to form said antenna and interconnections.

11. A method of making an interactive information closure in accordance with claim 9, including:

laser machining said lamination to form said antenna and said

15 interconnections.

12. A method of making an interactive information closure in accordance with claim 1, including:

positioning said integrated circuit active-side-down on said mounting

20 substrate with connection from the pads on said integrated circuit to the antenna or interconnections made directly by soldering, stud-bump bonding or with a conductive adhesive.

13. A method of making an interactive information closure in accordance with claim 1, including:

positioning said integrated circuit active-side-up on said mounting

25 substrate with connection from the pads on said integrated circuit to the antenna or interconnections made directly by wire bonding.

14. A method of making an interactive information closure in accordance with claim 1, wherein:

said step of positioning said microelectronics package on said mounting substrate includes first positioning said integrated circuit on said substrate, forming a planarization layer over said integrated circuit, forming one or more openings in said planarization layer, forming said antenna on said planarization layer, and forming said interconnections through said openings in said planarization layer.

30 35 15. A method of making an interactive information closure in accordance with claim 14, wherein:

said antenna and said interconnections are formed by metal deposition

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followed by photolithography.

16. A method of making an interactive information closure in accordance with claim 14, wherein:

5 said step of forming said openings includes photolithography or laser machining.

17. A method of making an interactive information closure in accordance with claim 1, wherein:

10 said step of positioning said microelectronics assembly on said mounting substrate including printing said integrated circuit with semi-conductor inks, and said antenna and said interconnections with electrically-conductive inks on said substrate.

18. A method of making an interactive information closure in accordance with claim 1, wherein:

10 said inserting step includes testing said microelectronics assembly.

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FIG. 1

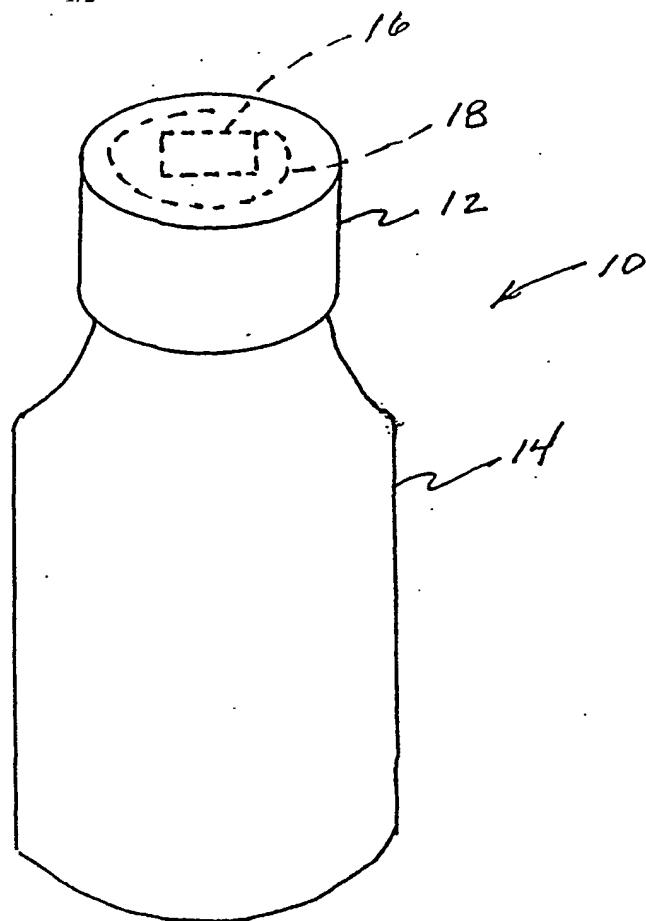


FIG. 2

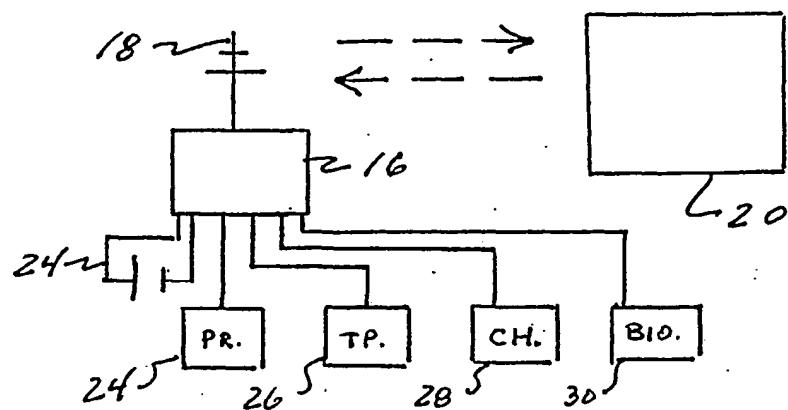
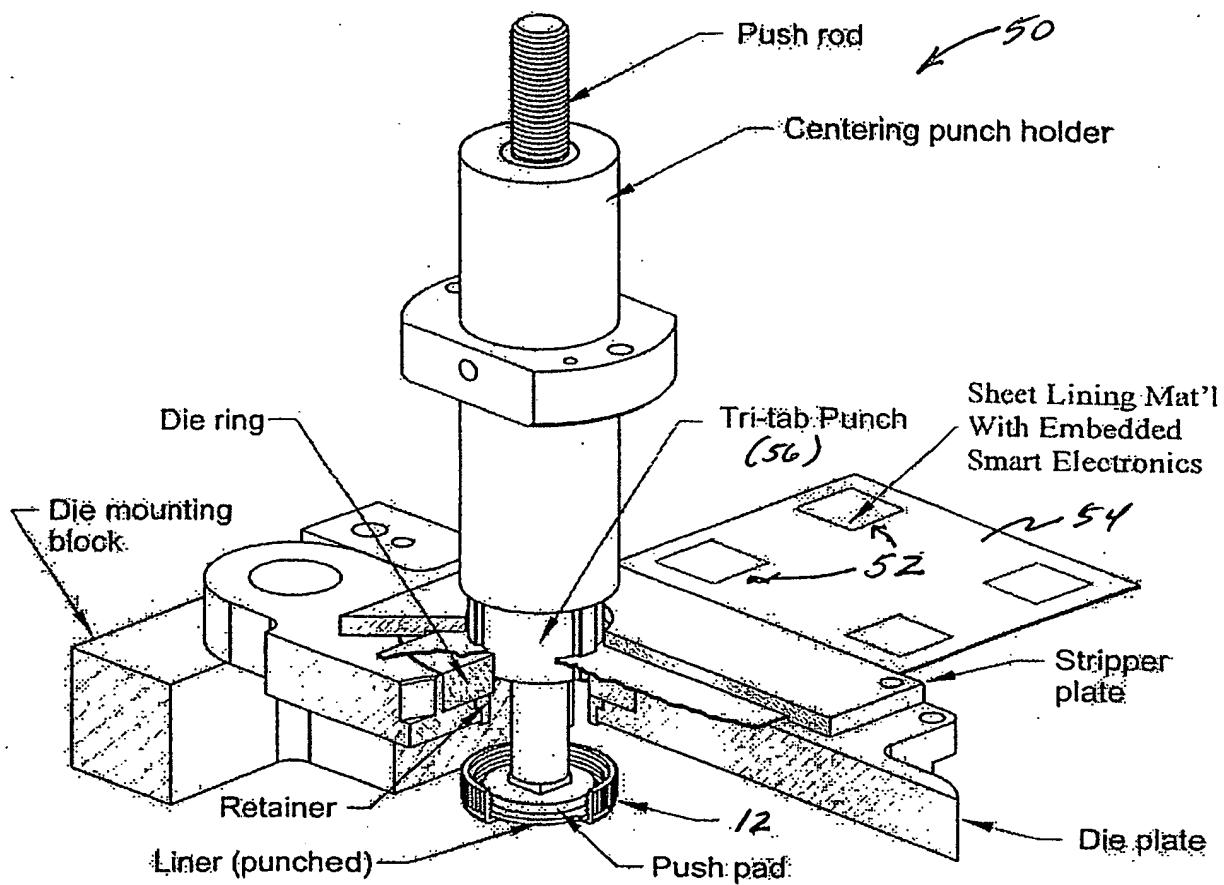


FIG. 3



INTERNATIONAL SEARCH REPORT

Intel National Application No
PCT/US 02/28921A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 G06K19/077 G06K7/00 G06K19/04 G01S13/00

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 7 G06K G01S

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the International search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	EP 0 619 243 A (LYONNAISE BOUCHAGE) 12 October 1994 (1994-10-12)	1-3, 18
Y	column 2, line 5 -column 3, line 44; figures 3,5 ---	4-17
Y	EP 0 615 285 A (CSIR) 14 September 1994 (1994-09-14) page 2, line 11 -page 3, line 11 page 6, line 46 -page 6, line 55 page 5, line 15 -page 5, line 36 abstract ---	4-6, 12, 15
Y	DE 197 23 847 A (FREUDENBERG CARL FA) 20 May 1998 (1998-05-20) claim 1 ---	7, 9, 10 -/-

 Further documents are listed in the continuation of box C. Patent family members are listed in annex.

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- *A* document defining the general state of the art which is not considered to be of particular relevance
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Date of the actual completion of the international search

Date of mailing of the International search report

12 December 2002

23/12/2002

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INTERNATIONAL SEARCH REPORT

International Application No
PCT/US 02/28921

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	DE 197 31 969 A (SIEMENS AG) 27 August 1998 (1998-08-27) column 1, line 53 -column 2, line 47 ---	8,11,16
Y	US 5 786 626 A (FAVREAU NORMAND GILLES ET AL) 28 July 1998 (1998-07-28) column 12, line 13 -column 12, line 22; figure 9 ---	13
Y	WO 01 03188 A (DUAN DAH WEIH ;BRADY MICHAEL JOHN (US); INTERMEC IP CORP (US)) 11 January 2001 (2001-01-11) page 7, line 22 -page 9, line 2; figures 4,5 ---	14
Y	US 5 818 019 A (DAIGLE STEVEN J ET AL) 6 October 1998 (1998-10-06) abstract ---	17
X	US 6 226 619 B1 (MOSKOWITZ PAUL ANDREW ET AL) 1 May 2001 (2001-05-01) column 3, line 64 -column 6, line 37 ----	1-3